



ON Semiconductor®

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MCH3375

Power MOSFET –30V, 295mΩ, –1.6A, Single P-Channel

Features

- On-Resistance $R_{DS(on)1}=227m\Omega$ (typ)
- 4V Drive
- High Speed Switching and Low Loss
- Pb-Free, Halogen Free and RoHS Compliance

Specifications

Absolute Maximum Ratings at $T_a = 25^\circ\text{C}$

Parameter	Symbol	Value	Unit
Drain to Source Voltage	V_{DSS}	–30	V
Gate to Source Voltage	V_{GSS}	± 20	V
Drain Current (DC)	I_D	–1.6	A
Drain Current (Pulse) $PW \leq 10\mu\text{s}$, duty cycle $\leq 1\%$	I_{DP}	–6.4	A
Power Dissipation When mounted on ceramic substrate ($900\text{mm}^2 \times 0.8\text{mm}$)	P_D	0.8	W
Junction Temperature	T_J	150	$^\circ\text{C}$
Storage Temperature	T_{stg}	–55 to +150	$^\circ\text{C}$

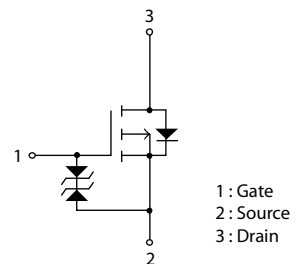
This product is designed to “ESD immunity < 200V*”, so please take care when handling.
* Machine Model

Thermal Resistance Ratings

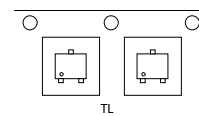
Parameter	Symbol	Value	Unit
Junction to Ambient When mounted on ceramic substrate ($900\text{mm}^2 \times 0.8\text{mm}$)	$R_{\theta JA}$	156.25	$^\circ\text{C/W}$

V_{DSS}	$R_{DS(on)}$ Max	I_D Max
–30V	295mΩ@ –10V	–1.6A
	523mΩ@ –4.5V	
	609mΩ@ –4V	

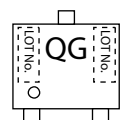
Electrical Connection P-Channel



Packing Type: TL



Marking



Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

ORDERING INFORMATION

See detailed ordering and shipping information on page 5 of this data sheet.

MCH3375

Electrical Characteristics at $T_a = 25^\circ\text{C}$

Parameter	Symbol	Conditions	Value			Unit
			min	typ	max	
Drain to Source Breakdown Voltage	$V_{(BR)DSS}$	$I_D = -1\text{mA}, V_{GS} = 0\text{V}$	-30			V
Zero-Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -30\text{V}, V_{GS} = 0\text{V}$			-1	μA
Gate to Source Leakage Current	I_{GSS}	$V_{GS} = \pm 16\text{V}, V_{DS} = 0\text{V}$			± 10	μA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = -10\text{V}, I_D = -1\text{mA}$	-1.2		-2.6	V
Forward Transconductance	g_{FS}	$V_{DS} = -10\text{V}, I_D = -0.8\text{A}$		1.3		S
Static Drain to Source On-State Resistance	$R_{DS(on)1}$	$I_D = -0.8\text{A}, V_{GS} = -10\text{V}$		227	295	$\text{m}\Omega$
	$R_{DS(on)2}$	$I_D = -0.4\text{A}, V_{GS} = -4.5\text{V}$		374	523	$\text{m}\Omega$
	$R_{DS(on)3}$	$I_D = -0.4\text{A}, V_{GS} = -4\text{V}$		435	609	$\text{m}\Omega$
Input Capacitance	C_{iss}	$V_{DS} = -10\text{V}, f = 1\text{MHz}$		82		pF
Output Capacitance	C_{oss}			22		pF
Reverse Transfer Capacitance	C_{rss}			16		pF
Turn-ON Delay Time	$t_{d(on)}$	See specified Test Circuit		4.0		ns
Rise Time	t_r			3.3		ns
Turn-OFF Delay Time	$t_{d(off)}$			12		ns
Fall Time	t_f			5.4		ns
Total Gate Charge	Q_g		$V_{DS} = -15\text{V}, V_{GS} = -10\text{V}, I_D = -1.6\text{A}$		2.2	
Gate to Source Charge	Q_{gs}			0.36		nC
Gate to Drain "Miller" Charge	Q_{gd}			0.49		nC
Forward Diode Voltage	V_{SD}	$I_S = -1.6\text{A}, V_{GS} = 0\text{V}$		-0.9	-1.5	V

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

Switching Time Test Circuit

