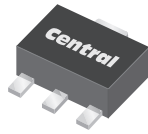


**CXDM4060P**  
**SURFACE MOUNT SILICON**  
**P-CHANNEL**  
**ENHANCEMENT-MODE**  
**MOSFET**



**SOT-89 CASE**



[www.centrasemi.com](http://www.centrasemi.com)

**DESCRIPTION:**

The CENTRAL SEMICONDUCTOR CXDM4060P is a high current silicon P-Channel enhancement-mode MOSFET, designed for high speed pulsed amplifier and driver applications. This MOSFET features high current, low  $r_{DS(ON)}$ , low threshold voltage, and low gate charge.

**MARKING: FULL PART NUMBER**

**APPLICATIONS:**

- Load/Power switches
- Power supply converter circuits
- Battery powered portable equipment

**FEATURES:**

- Low  $r_{DS(ON)}$  (48mΩ TYP @  $V_{GS}=10V$ )
- High current ( $I_D=6.0A$ )
- Logic level compatibility

**MAXIMUM RATINGS:** ( $T_A=25^\circ C$ )

Drain-Source Voltage
Gate-Source Voltage
Continuous Drain Current (Steady State)
Maximum Pulsed Drain Current, $t_p=10\mu s$
Power Dissipation
Operating and Storage Junction Temperature
Thermal Resistance

SYMBOL		UNITS
$V_{DS}$	40	V
$V_{GS}$	25	V
$I_D$	6.0	A
$I_{DM}$	20	A
$P_D$	1.2	W
$T_J, T_{stg}$	-55 to +150	$^\circ C$
$\theta_{JA}$	104	$^\circ C/W$

**ELECTRICAL CHARACTERISTICS:** ( $T_A=25^\circ C$  unless otherwise noted)

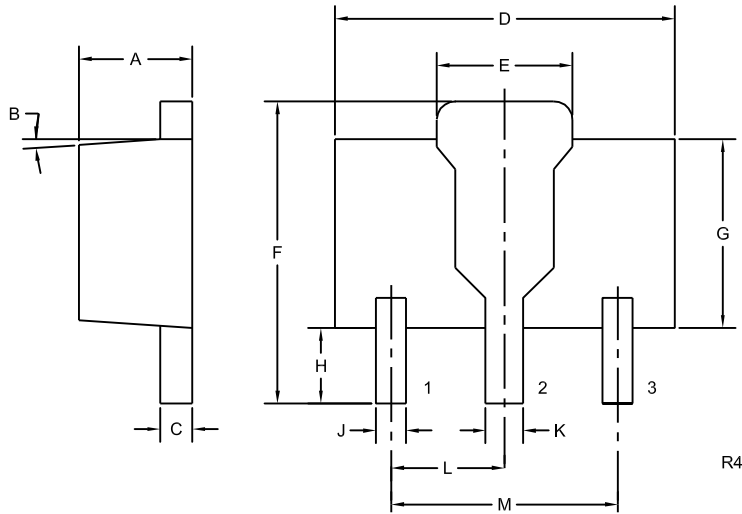
SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNITS
$I_{GSSF}, I_{GSSR}$	$V_{GS}=25V, V_{DS}=0$			100	nA
$I_{DSS}$	$V_{DS}=40V, V_{GS}=0$			1.0	$\mu A$
$BV_{DSS}$	$V_{GS}=0, I_D=250\mu A$	40			V
$V_{GS(th)}$	$V_{GS}=V_{DS}, I_D=250\mu A$	1.0	2.0	3.0	V
$V_{SD}$	$V_{GS}=0, I_S=2.0A$			1.2	V
$r_{DS(ON)}$	$V_{GS}=10V, I_D=6.0A$		48	65	mΩ
$r_{DS(ON)}$	$V_{GS}=4.5V, I_D=4.0A$		80	95	mΩ
$C_{rss}$	$V_{DS}=25V, V_{GS}=0, f=1.0MHz$		61		pF
$C_{iss}$	$V_{DS}=25V, V_{GS}=0, f=1.0MHz$		750		pF
$C_{oss}$	$V_{DS}=25V, V_{GS}=0, f=1.0MHz$		56		pF
$Q_g(tot)$	$V_{DS}=32V, V_{GS}=4.5V, I_D=6.0A$		6.5		nC
$Q_{gs}$	$V_{DS}=32V, V_{GS}=4.5V, I_D=6.0A$		3.2		nC
$Q_{gd}$	$V_{DS}=32V, V_{GS}=4.5V, I_D=6.0A$		2.7		nC
$t_{on}$	$V_{DS}=20V, V_{GS}=10V, I_D=1.0A$		18		ns
$t_{off}$	$R_G=3.0\Omega, R_L=20\Omega$		64		ns

R1 (28-March 2013)

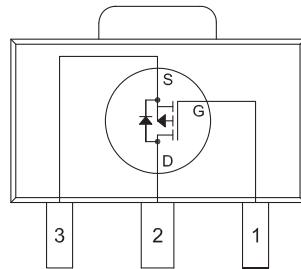
**CXDM4060P**  
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**MOSFET**



**SOT-89 CASE - MECHANICAL OUTLINE**



**PIN CONFIGURATION**



(Top View)  
 Tab is common to pin 2

**LEAD CODE:**

- 1) Gate
- 2) Drain
- 3) Source

**MARKING: FULL PART NUMBER**

SYMBOL	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.055	0.067	1.40	1.70
B	4°		4°	
C	0.014	0.018	0.35	0.46
D	0.173	0.185	4.40	4.70
E	0.064	0.074	1.62	1.87
F	0.146	0.177	3.70	4.50
G	0.090	0.106	2.29	2.70
H	0.028	0.051	0.70	1.30
J	0.014	0.019	0.36	0.48
K	0.017	0.023	0.44	0.58
L	0.059		1.50	
M	0.118		3.00	

SOT-89 (REV: R4)

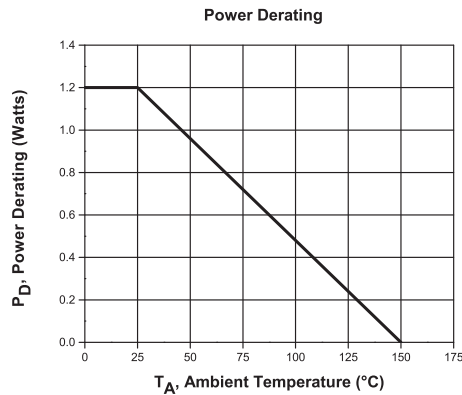
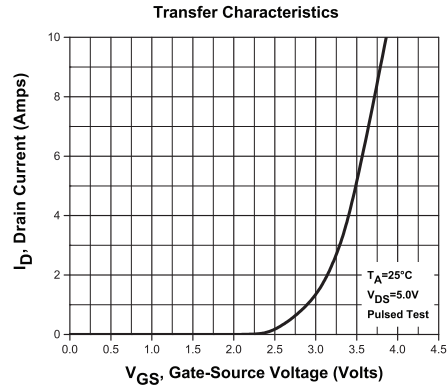
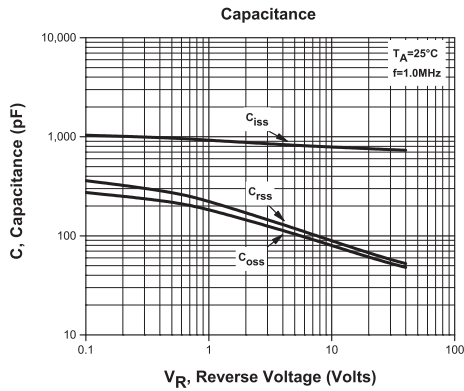
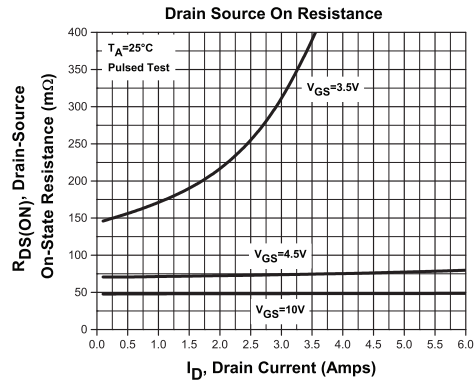
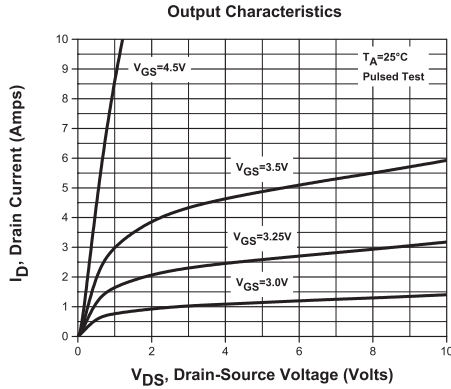
R1 (28-March 2013)

CXDM4060P

SURFACE MOUNT SILICON  
P-CHANNEL  
ENHANCEMENT-MODE  
MOSFET



TYPICAL ELECTRICAL CHARACTERISTICS



R1 (28-March 2013)

## OUTSTANDING SUPPORT AND SUPERIOR SERVICES



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### PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- Inventory bonding
- Consolidated shipping options
- Custom bar coding for shipments
- Custom product packing

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### DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free quick ship samples (2<sup>nd</sup> day air)
- Online technical data and parametric search
- SPICE models
- Custom electrical curves
- Environmental regulation compliance
- Customer specific screening
- Up-screening capabilities
- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- Application and design sample kits
- Custom product and package development

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### CONTACT US

#### Corporate Headquarters & Customer Support Team

Central Semiconductor Corp.  
145 Adams Avenue  
Hauppauge, NY 11788 USA  
Main Tel: (631) 435-1110  
Main Fax: (631) 435-1824  
Support Team Fax: (631) 435-3388  
[www.centalsemi.com](http://www.centalsemi.com)

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[www.centalsemi.com/wwreps](http://www.centalsemi.com/wwreps)

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